List of reference symbols

10	MIM capacitor arrangement
12	Main capacitor
14	Disconnectable capacitor
16	Connectable capacitor
18, 20, 22	Bottom electrode near the substrate
24, 26, 28	Electrode remote from the substrate
30	Contact
32, 34.	Interconnect
36, 38	Cutout
40, 42	Region
52, 54	Interconnect
56, 58	Cutout
60, 62	Linking region
La to Lc	Length
C(0), $Cm(1)$, $Cp(1)$	Capacitance
110	Grid capacitor arrangement
112	Main capacitor
114	Disconnectable capacitor
116	Connectable capacitor
118	Longitudinal electrode
120 to 126	Transverse electrode
128	Longitudinal electrode
130 to 136	Transverse electrode
140 to 146	Longitudinal electrode
150 to 160	Transverse electrode
162	Region
164	Cutout
166	Region
168	Cutout
170, 172	Region
174, 176	Cutout
180, 182	Linking region
184, 186	Cutout
190, 192	Linking region
194, 196	Cutout
200	Transverse electrode

202, 204	Region
206, 208	Cutout
220	Transverse electrode
222	Cutout
300	Determination of the capacitance
	variation
302	Provide correction possibility
304	Start
306	Wafer processing
308	Detection of the actual capacitance
310	Actual equal to desired?
312	Actual > desired?
314	Interruption
316	Linking
318	Passivation
320	Singulation
322	End